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Séminaires de l'IDRIS

RECENT TRENDS IN HIGH PERFORMANCE MICROPROCESSOR ARCHITECTURE

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During the last 5 years, thanks to huge progress in Integrated Circuit (IC) technology, microprocessor architecture has made considerable progress. More recently, the thermal wall (limiting the increase in frequency) has put more focus on microprocessor architecture pushing ahead multicore. In this lecture we will review most of recent progress in microprocessor architecture (out of order execution, superscalar, vector extensions, multi/hyperthreading, multilevel caches, multicore). The emphasis will be set on the impact of these new features on programming and performance tuning.

William Jalby est directeur du LRC IT@CA (laboratoire commun CEA/DAM – université de Versailles-Saint Quentin).

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